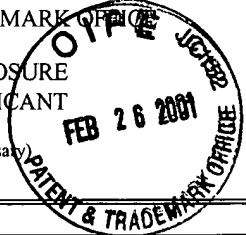


FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE (Modified) PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 1662-27800 (P00-3105)	SERIAL NO. 09/652,834
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		APPLICANTS Michael S. BERTONE et al.	
(37 CFR 1.98(b)) (Use several sheets if necessary)		FILING DATE August 31, 2000	GROUP 2152



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Examiner Initial		Patent Number							Issue Date	Patentee	Class	Sub-class	Filing Date (If Appropriate)
ALL	AA	5	2	6	1	0	6	6	11/09/93	Jouppi et al.	395	425	03/27/90
ALL	AB	5	3	1	7	7	1	8	05/31/94	Jouppi	395	425	01/25/93
ALL	AC	5	7	5	8	1	8	3	05/26/98	Scales	395	825	07/17/96
ALL	AD	5	7	6	1	7	2	9	06/02/98	Scales	711	148	07/17/96
ALL	AE	5	7	8	7	4	8	0	07/28/98	Scales et al.	711	148	07/17/96
ALL	AF	5	8	0	2	5	8	5	09/01/98	Scales et al.	711	154	07/17/96
ALL	AG	5	8	0	9	4	5	0	09/15/98	Chrysos et al.	702	186	11/26/97
ALL	AH	5	8	7	5	1	5	1	02/23/99	Mick	365	233	05/28/97
ALL	AI	5	8	9	0	2	0	1	03/30/99	McLellan et al.	711	108	07/01/97
ALL	AJ	5	8	9	3	9	3	1	04/13/99	Peng et al.	711	206	01/15/97
ALL	AK	5	9	1	8	2	5	0	06/29/99	Hammond	711	205	05/05/95
ALL	AL	5	9	1	8	2	5	1	06/29/99	Yamada et al.	711	207	12/23/96
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ALL	AN	5	9	5	0	2	2	8	09/07/99	Scales et al.	711	148	02/03/97
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ALL	AR	6	0	7	0	2	2	7	05/30/2000	Rokicki	711	117	10/31/97
ALL	AS	6	0	8	5	3	0	0	07/04/2000	Sunaga et al.	711	168	09/19/97

FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION													
		Document Number							Publication Date	Country or Patent Office	Class	Sub-Class	Translation
													Yes No

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OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place of Publication)		
All	AT	<i>Alpha Architecture Reference Manual</i> , Third Edition, The Alpha Architecture Committee, 1998 Digital Equipment Corporation (21 p.), in particular pages 3-1 through 3-15
All	AU	<i>A Logic Design Structure For LSI Testability</i> , E. B. Eichelberger et al., 1977 IEEE (Pages 462-468)
All	AV	<i>Direct RDRAM™ 256/288-Mbit (512Kx16/18x32s)</i> , Preliminary Information Document DL0060 Version 1.01 (69 p.)
All	AW	<i>Testability Features of AMD-K6™ Microprocessor</i> , R. S. Fetherston et al., Advanced Micro Devices (8 p.)
All	AX	<i>Hardware Fault Containment in Scalable Shared-Memory Multiprocessors</i> , D. Teodosiu et al., Computer Systems Laboratory, Stanford University (12 p.), 1977
All	AY	<i>Cellular Disco: resource management using virtual clusters on shared-memory multiprocessors</i> , K. Govil et al., 1999 ACM 1-58113-140-2/99/0012 (16 p.)
All	AZ	<i>Are Your PLDs Metastable?</i> , Cypress Semiconductor Corporation, March 6, 1997 (19 p.)
All	BA	<i>Rambus® RIMM™ Module (with 128/144Mb RDRAMs)</i> , Preliminary Information, Document DL0084 Version 1.1 (12 p.)
All	BB	<i>Direct Rambus™ RIMM™ Module Specification Version 1.0</i> , Rambus Inc., SL-0006-100 (32 p.), 2000
All	BC	<i>End-To-End Fault Containment In Scalable Shared-Memory Multiprocessors</i> , D. Teodosiu, July 2000 (148 p.)
Examiner	<i>Christine D. Fogle</i> Date Considered 10.22.03	
EXAMINER: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.		